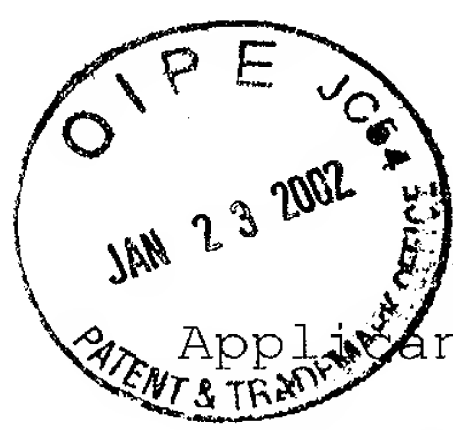


23
04-4-02

PATENT
PF-2802



IN THE U.S. PATENT AND TRADEMARK OFFICE

Applicant: Minoru IWAMOTO Conf.: 2650
Appl. No.: 09/864,445 Group: 2816
Filed: May 25, 2001 Examiner: UNASSIGNED
For: SEMICONDUCTOR INTEGRATED CIRCUIT HAVING
FUNCTIONAL MACRO WITH IMPROVED POWER LINE
CONNECTION STRUCTURE

RECEIVED
JAN 25 2002
TC 2800 MAIL ROOM

CLAIM TO PRIORITY

Assistant Commissioner for Patents
Washington, DC 20231

Date: January 23, 2002

Sir:

Applicant(s) herewith claim(s) the benefit of the
priority filing date of the following application(s) for the
above-entitled U.S. application under the provisions of 35
U.S.C. § 119 and 37 C.F.R. § 1.55:

<u>Country</u>	<u>Application No.</u>	<u>Filed</u>
JAPAN	2000-157932	May 29, 2000

Certified copy(ies) of the above-noted application(s)
is(are) attached hereto.

Respectfully submitted,

YOUNG & THOMPSON

Benoit Castel

Benoit Castel, Reg. No. 35,041

745 South 23rd Street
Arlington, VA 22202
Telephone (703) 521-2297

BC/mdp

Attachment(s): 1 Certified Copy(ies)

ARTIFACT SHEET

Indicate quantity of item(s) received by the USPTO but not scanned.

- ☐ Priority Document No. 2000157932 Country JAPAN
Doc Code: FRPR
- ☐ CD(s) containing computer program listing
Doc Code: Computer
- ☐ Stapled Set(s) of Extra Color Drawings
Doc Code: Artifact
- ☐ CD(s) containing pages of specification ☐
and/or sequence listing ☐
Doc Code: Artifact
- ☐ CD(s) with content unspecified
Doc Code: Artifact
- ☐ Microfilm(s)
Doc Code: Artifact
- ☐ Video tape(s)
Doc Code: Artifact
- ☐ Other mass storage media, description: _____
Doc Code: Artifact
- ☐ Model(s)
Doc Code: Artifact
- ☐ Other, description: _____
Doc Code: Artifact
- ☐ Other, description: _____
Doc Code: Artifact
- ☐ Other, description: _____
Doc Code: Artifact